

Project Title	Design and Development of Application-Specific FPGA/Reconfigurable Hardware Generator.
Principle Investigator (PI)	Dr. Muhammad Mohiuddin
Principle Investigator, Institute/University (PIO)	PAF-Karachi Institute of Economics and Technology, Karachi
Number of Publications	03

Publication

- 1. Research Paper Title:** Diffusion-Based Placement Algorithm for Reducing High Interconnect Demand in Congested Regions of FPGAs, **Journal/Conference:** Reconfigurable Computing: Architectures, Tools, and Applications, Lecture Notes in Computer Science, Volume 8405, (10th International Symposium, ARC 2014, Vilamoura, Portugal, April 14-16), 2014, pages 291-297.
Name of Authors: Ali Asghara, Husain Parveza a College of Engineering, PAF-KIET Karachi Institute of Economics and Technology, Pakistan.
- 2. Research Paper Title:** Exploring alternate trade-offs of placement quality versus runtime in Simulated Annealing algorithm **Name of Journal/Conference:** 9th International Symposium on Reconfigurable Communication-centric Systems-on-Chip (ReCoSoC 2014), 26-28 May 2014, Montpellier, France, Pages 1-5 **Name of Authors:** Baqar Razaa, Husain Parveza , Muhammad Mohiuddina a College of Engineering, PAF-KIET Karachi Institute of Economics and Technology, Pakistan
- 3. Research Paper Title:** Design-space exploration between FPGA and ASIF **Name of Journal/Conference:** 9th International Symposium on Reconfigurable Communication-centric Systems-on-Chip (ReCoSoC 2014), 26-28 May 2014, Montpellier, France, Pages 1-5 **Name of Authors:** Muhammad Ameen Qureshia, Husain Parveza a College of Engineering, PAF-KIET Karachi Institute of Economics and Technology, Pakistan